

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Divisional Patent Application of  
U.S. Appl. No.: 09/466,969:

Yoshiharu NAKAJIMA et al.

Examiner: Xiao Min Wu

Application No.: Not Yet Assigned

Art Unit: 2674

Filed: December 15, 2003

For: DIGITAL/ANALOG CONVERTER CIRCUIT, LEVEL SHIFT CIRCUIT, SHIFT  
REGISTER UTILIZING LEVEL SHIFT CIRCUIT SAMPLING LATCH  
CIRCUIT, LATCH CIRCUIT, AND LIQUID CRYSTAL DISPLAY DEVICE  
INCORPORATING THE SAME

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**CLAIM FOR PRIORITY**

**MS PATENT APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The benefit of the filing dates of the following prior applications filed in the following foreign country is hereby requested and the right of priority provided under 35 U.S.C. §119 is hereby claimed:

Japanese Patent Appl. No. P10-362283 filed December 21, 1998

Japanese Patent Appl. No. P11-023383 filed February 1, 1999

Japanese Patent Appl. No. P11-023384 filed February 1, 1999

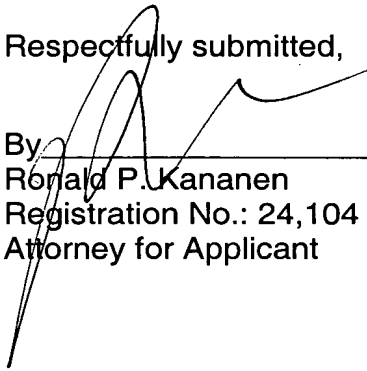
Japanese Patent Appl. No. P11-023382 filed February 1, 1999

Japanese Patent Appl. No. P11-299188 filed October 21, 1999

In support of this claim, filed within parent application Serial No. 09/466,969 is a certified copy of said original foreign applications.

Dated: December 15, 2003

Respectfully submitted,

By   
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